

ABSTRACT OF THE DISCLOSURE

POWER SUPPLY UNIT AND COMPUTER

A series power supply circuit and a switching power supply circuit are combined within a single power supply unit. The switching power supply circuit provides an efficiency lower than that of the series power supply circuit under a light load and provides an efficiency higher than that of the series power supply circuit under a heavy load. A standby signal that is asserted under a light load and is deasserted under a heavy load is input to a negative logic enable terminal (-EN) of the series power supply circuit through an inverter0. The standby signal is directly input to the negative logic enable terminal (-EN) of a PWM controller in the switching power supply circuit.